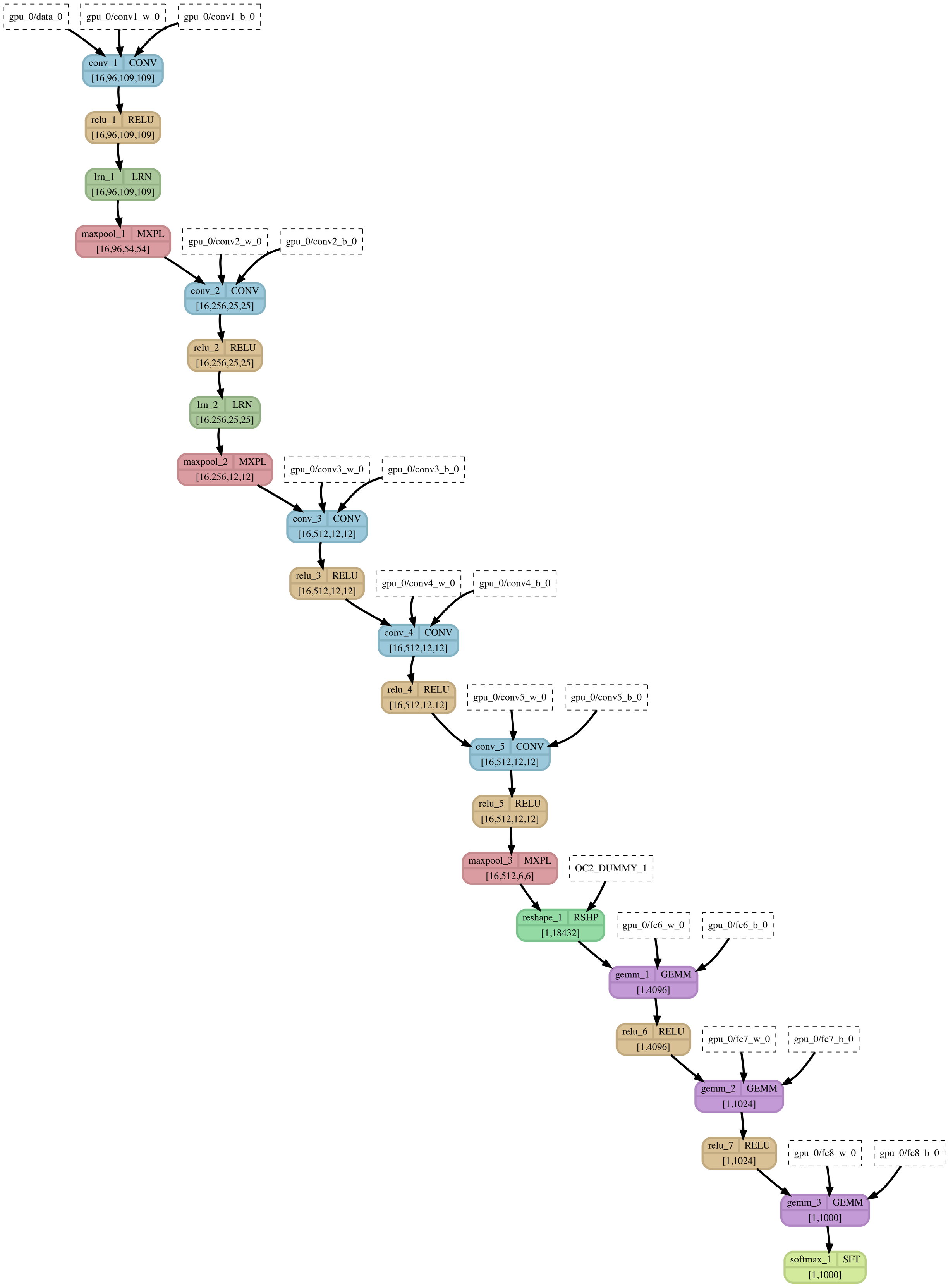
gpu_0/data_0
gpu_0/conv1_w_0
gpu_0/conv1_b_0
conv_1 CONV [16,96,109,109]
relu_1 RELU [16,96,109,109]
lrn_1 LRN [16,96,109,109]
maxpool_1 MXPL [16,96,54,54]
gpu_0/conv2_w_0
gpu_0/conv2_b_0
conv_2 CONV [16,256,25,25]
relu_2 RELU [16,256,25,25]
lrn_2 LRN [16,256,25,25]
maxpool_2 MXPL [16,256,12,12]
gpu_0/conv3_w_0
gpu_0/conv3_b_0
conv_3 CONV [16,512,12,12]
relu_3 RELU [16,512,12,12]
gpu_0/conv4_w_0
gpu_0/conv4_b_0
conv_4 CONV [16,512,12,12]
relu_4 RELU [16,512,12,12]
gpu_0/conv5_w_0
gpu_0/conv5_b_0
conv_5 CONV [16,512,12,12]
relu_5 RELU [16,512,12,12]
maxpool_3 MXPL [16,512,6,6]
OC2_DUMMY_1
reshape_1 RSHP [1,18432]
gpu_0/fc6_w_0
gpu_0/fc6_b_0
gemm_1 GEMM [1,4096]
relu_6 RELU [1,4096]
gpu_0/fc7_w_0
gpu_0/fc7_b_0
gemm_2 GEMM [1,1024]
relu_7 RELU [1,1024]
gpu_0/fc8_w_0
gpu_0/fc8_b_0
gemm_3 GEMM [1,1000]
softmax_1 SFT [1,1000]